



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **YAMASHITA, Tomio**

Serial No.: **10/076,355**

Filed: **February 19, 2002**

Group Art Unit: 2811

Examiner: VU, Hung

P.T.O. Confirmation No.: 9518

For: **SEMICONDUCTOR DEVICE AND METHOD FOR MANUFACTURING
SEMICONDUCTOR DEVICE**

RESPONSE TO THE RESTRICTION REQUIREMENT
DATED November 5, 2002

Commissioner for Patents
Washington, D.C. 20231

Date: December 3, 2002

Sir:

This paper is submitted in response to the Official Action dated **November 5, 2002**.

In the Action, restriction is required between Group (I), Claims 1-4 and 6-10 and Group (II), Claims 5 and 11.

Applicant hereby elects the subject matter of Group (I), Claims 1-4 and 6-10 for prosecution in this application. This election is made without traverse, it being understood that the applicant's right to the filing of a divisional application directed to the non-elected subject matter under 35 USC 120 and 35 USC 121 are retained.

In the event that this paper is not timely filed, applicants hereby petition for an appropriate extension of time. The fee for any such extension may be charged to our Deposit Account No. 01-2340.

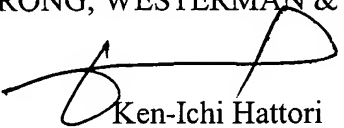
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Serial No.: 10/076,355

In the event any additional fees are required in connection with this response, please charge our Deposit Account No. 01-2340.

Respectfully submitted,

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